

# SFP-M(-T)-TAA

## GIGABIT SFP MODULE WITH TAA COMPLIANT

### FEATURES

- SFP Multi-Source Agreement Compliant
- LC Duplex Receptacle
- 1.0625Gbps Fibre Channel  
FC-PI 100-M5-SN-I Compliant
- 1.0625Gbps Fibre Channel  
FC-PI 100-M6-SN-I Compliant
- 1.25Gbps IEEE 802.3ah  
1000BASE-SX Compliant
- SFF-8472 Diagnostic Monitoring Interface
- Alarms and Warnings to Indicate Status of Real Time Monitors
- Class 1 Laser Safety Standard  
IEC 60825 Compliant
- TAA Compliant

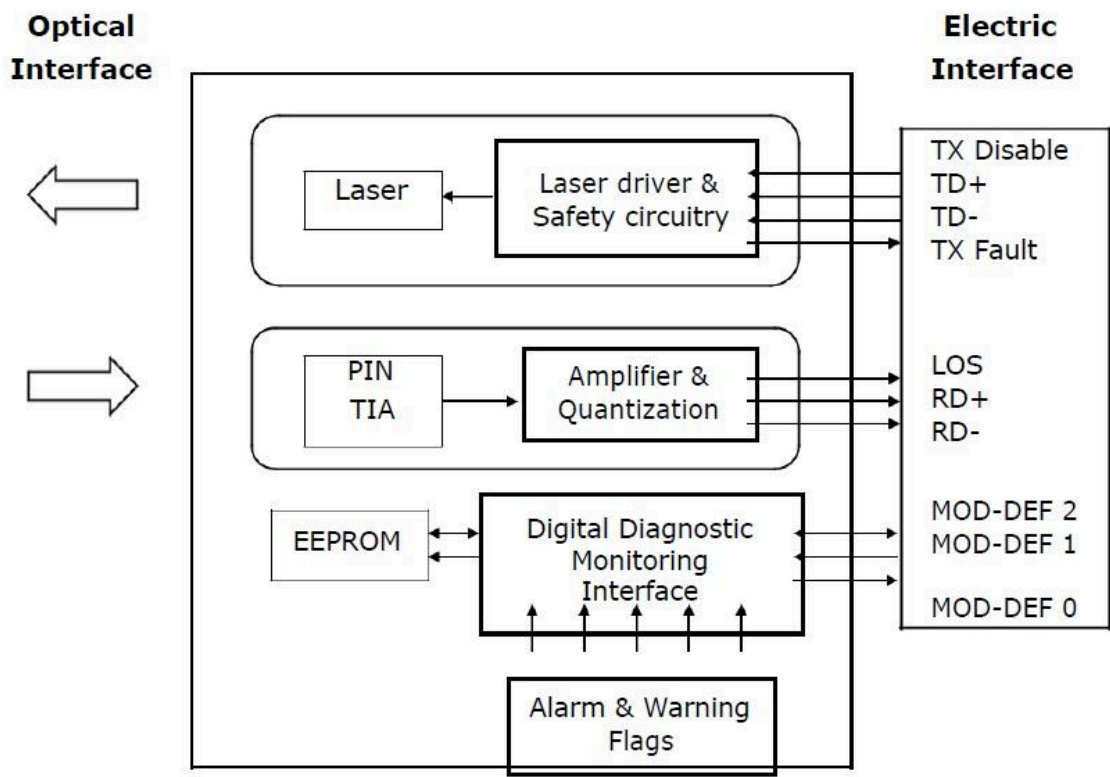


### INTRODUCTION

The SFP-M(-T)-TAA is specifically designed for the high performance integrated duplex data link over multimode optical fiber. This transceiver module is compliant with the Small Form-factor Pluggable (SFP) Multisource Agreement (MSA). An enhanced Digital Diagnostic Monitoring Interface has been incorporated into the SFP Transceiver. Real time monitors of temperature, supply voltage, laser bias current, laser average output power and received output power are provided, based on the SFF-8472.

The SFP-M(-T)-TAA transceivers using a short wavelength (850nm) VCSEL laser diode enable data transmission up to 550m on a multimode 50/125µm optical fiber and 300m on a multimode 62.5/125µm optical fiber.

BLOCK DIAGRAM



The transceiver fundamentally consists of two parts: transmitter and receiver. The transmitter features a TTL logic level Disable signal and a Fault indicator. The receiver features a TTL logic Loss of Signal (LOS) detection. The serial ID interface defines a 256-byte memory map in EEPROM, accessible over a 2 wire, serial interface at the 8 bit address 1010000X (A0h). The Digital Diagnostic Monitoring Interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged and is therefore backward compatible. The detailed signal descriptions are listed in the following sections.

SPECIFICATIONS

Absolute Maximum Ratings					
Parameter	Symbol	Minimum	Maximum	Unit	Note
Storage Temperature	Ts	-40	85	°C	
Supply Voltage	VccT VccR	-0.5	4.0	V	
Relative Humidity	RH	5	95	%	

Recommended Operating Conditions						
Parameter	Symbol	Minimum	Type	Maximum	Unit	Note
Case Operating Temperature	Tc	0		70	°C	Refer to Ordering Information
		-40		85		
Supply Voltage	Vcc	3.1	3.3	3.5	V	
Supply Current	I <sub>TX</sub> + I <sub>RX</sub>		150	300	mA	

## Transmitter Electro-Optical Interface

Parameter	Symbol	Minimum	Type	Maximum	Unit	Note
Transmitter Differential Input Voltage	TD+/-	400		2400	mV	
Tx_Fault - High	V <sub>Fault_H</sub>	2		V <sub>cc</sub>	V	
Tx_Fault - Low	V <sub>Fault_L</sub>	V <sub>ee</sub>		V <sub>ee</sub> + 0.8	V	
Tx_Disable - High	V <sub>DISABLE_H</sub>	2		V <sub>cc</sub>	V	
Tx_Disable - Low	V <sub>DISABLE_L</sub>	V <sub>ee</sub>		V <sub>ee</sub> + 0.8	V	
Optical Output Power	P <sub>O</sub>	-9.5		-4	dBm	1
Optical Extinction Ratio	E <sub>R</sub>	9			dB	
Center Wavelength	λ <sub>C</sub>	830	850	860	nm	
Spectral Width (RMS)	Δλ			0.85	nm	
Optical Rise / Fall Time	t <sub>r</sub> / t <sub>f</sub>			260	ps	2
Relative Intensity Noise	RIN			-117	dB/Hz	
Total Contributed Jitter	TJ			227	ps	
Coupled Power Ratio	CPR	9			dB	

### Notes:

1. Coupling into a 50/125μm multimode fiber.
2. 20% to 80% value.

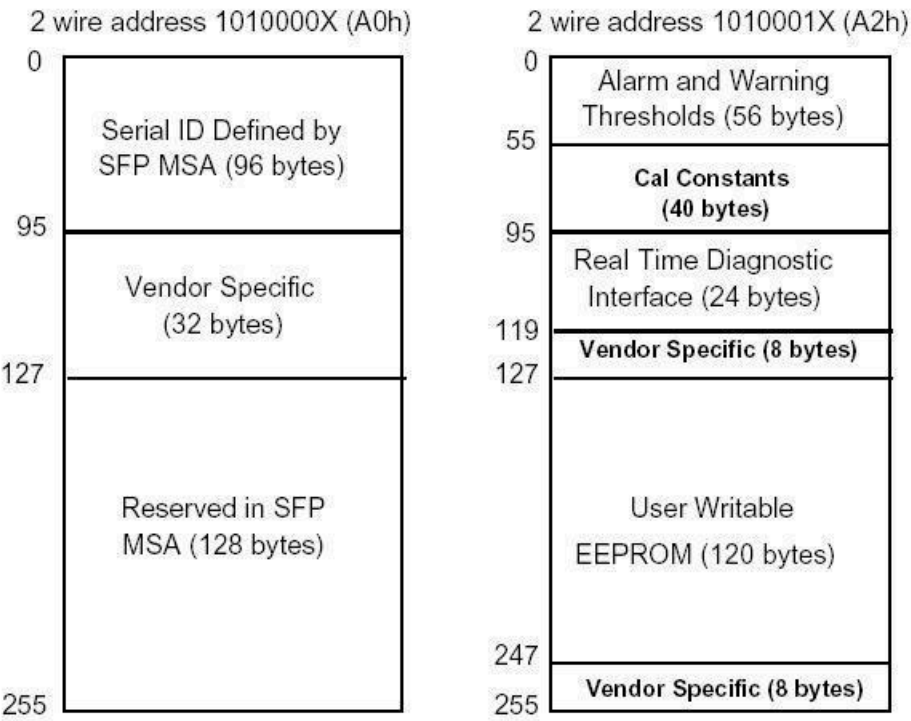
## Receiver Electro-Optical Interface

Parameter	Symbol	Minimum	Type	Maximum	Unit	Note
Receiver Differential Output Voltage	RD +/-	400		2000	mV	
Receiver Overload	P <sub>IN</sub> MAX	-3			dBm	1
Receiver Sensitivity	P <sub>IN</sub> MIN			-17	dBm	1
Operating Center Wavelength	λ <sub>C</sub>	770		860	nm	
Return Loss	RL	12			dB	
Receiver Loss of Signal - TTL Low	P <sub>RX_LOSD</sub>			-17.5	dBm	
Receiver Loss of Signal - TTL High	P <sub>RX_LOSA</sub>	-35			dBm	
Receiver Loss of Signal - Hysteresis	P <sub>RX_LOSH</sub>	0.5			dB	

### Notes:

1. With BER better than or equal to  $1 \times 10^{-12}$ , measured in the center of the eye opening with 2<sup>7</sup>-1 PRBS

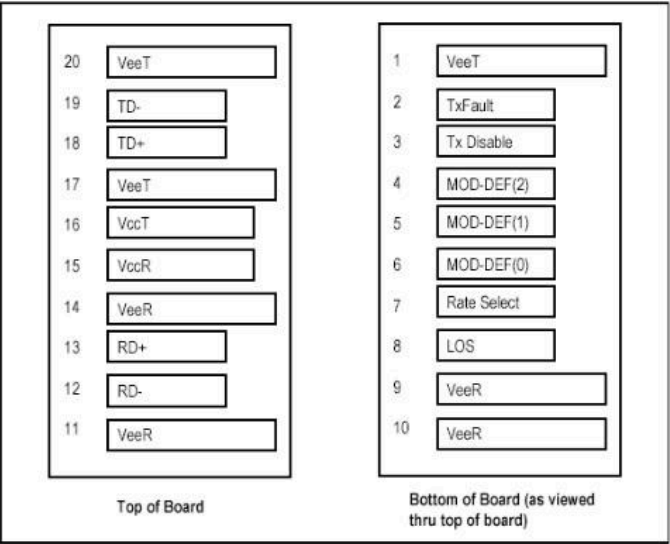
DIGITAL DIAGNOSTIC MEMORY MAP



DIGITAL DIAGNOSTIC MONITORING CHARACTERISTICS

Parameter	Symbol	Accuracy	Unit	Note
Transceiver Temperature	T <sub>INT</sub>	±3	°C	
Transceiver Supply Voltage	V <sub>INT</sub>	±3	%	
TX Bias Current	I <sub>BIAS</sub>	±10	%	
TX Output Power	P <sub>TX</sub>	±3	dB	
RX Received Optical Power	P <sub>RX</sub>	±3	dB	

PIN DESCRIPTION



SFP Transceiver Electric Pad Layout

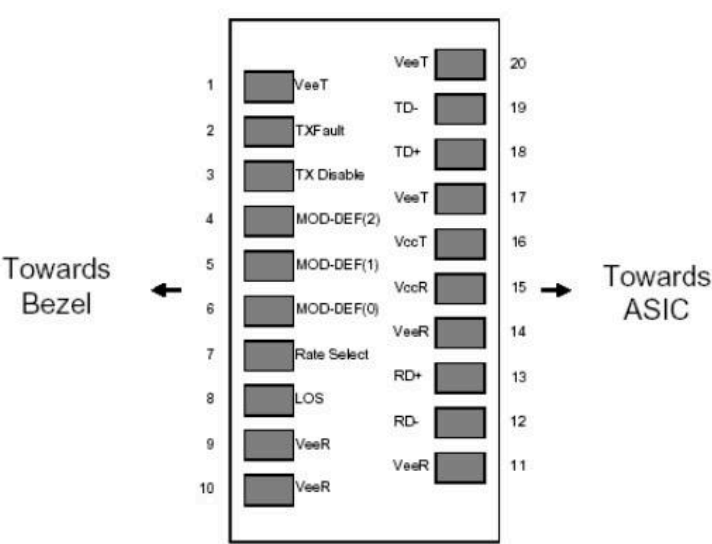


Diagram of Host Board Connector Block Pin Numbers and Names

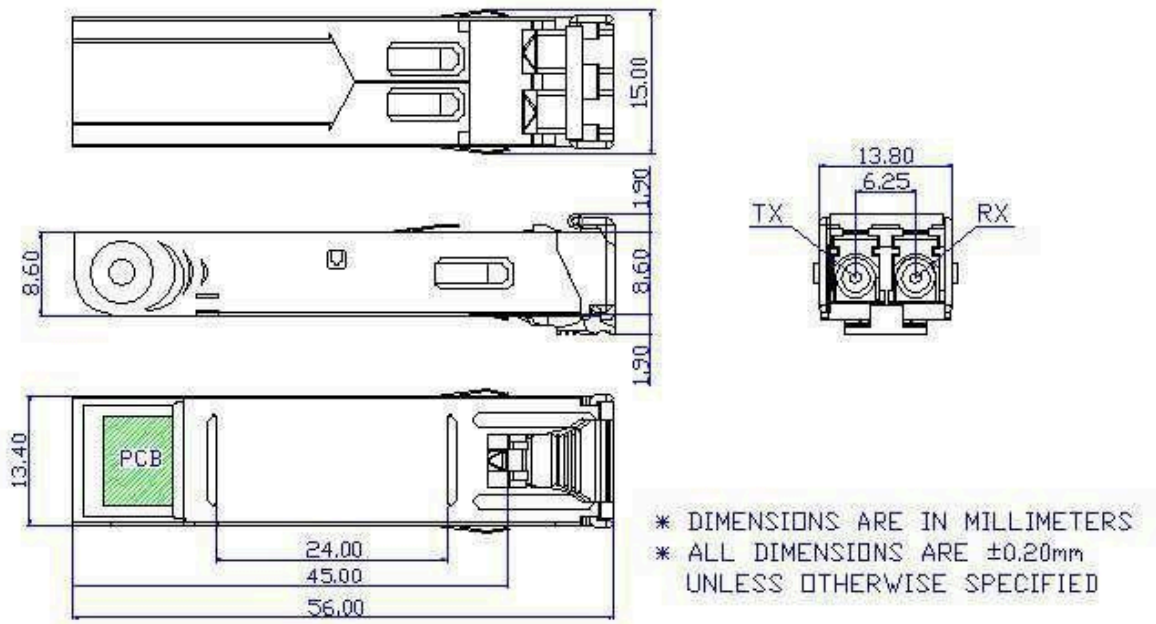
Pin Number	Pin Name	Function	Plug Sequence	Notes
1	VeeT	Transmitter Ground	1	
2	TX Fault	Transmitter Fault Indication	3	1
3	TX Disable	Transmitter Disable	3	2
4	MOD-DEF 2	Module Definition 2	3	3
5	MOD-DEF 1	Module Definition 1	3	3
6	MOD-DEF 0	Module Definition 0	3	3
7	Rate Select	Select between full or reduced receiver bandwidth	3	4
8	LOS	Loss of Signal	3	5
9	VeeR	Receiver Ground	1	6
10	VeeR	Receiver Ground	1	6
11	VeeR	Receiver Ground	1	6
12	RD-	Inv. Receiver Data Out	3	7
13	RD+	Receiver Data Out	3	7
14	VeeR	Receiver Ground	1	6
15	VccR	Receiver Power	2	8
16	VccT	Transmitter Power	2	8
17	VeeT	Transmitter Ground	1	6
18	TD+	Transmitter Data In	3	9
19	TD-	Inv. Transmitter Data In	3	9
20	VeeT	Transmitter Ground	1	6

**Notes:**

Plug Sequence: Pin engagement sequence during hot plugging.

1. TX Fault is an open collector/drain output, which should be pulled up with a 4.7K – 10K $\Omega$  resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
2. TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7 – 10 K $\Omega$  resistor. Its states are:  
Low (0 – 0.8V): Transmitter on  
(>0.8, < 2.0V): Undefined  
High (2.0 – 3.465V): Transmitter Disabled  
Open: Transmitter Disabled
3. Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K – 10K $\Omega$  resistor on the host board. The pull-up voltage shall be VccT or VccR  
Mod-Def 0 is grounded by the module to indicate that the module is present  
Mod-Def 1 is the clock line of two wire serial interface for serial ID  
Mod-Def 2 is the data line of two wire serial interface for serial ID
4. This is an optional input used to control the receiver bandwidth for compatibility with multiple data rates (most likely Fiber Channel 1x and 2x Rates). If implemented, the input will be internally pulled down with > 30k $\Omega$  resistor. The input states are:  
Low (0 – 0.8V): Reduced Bandwidth  
(>0.8, < 2.0V): Undefined  
High (2.0 V – 3.465V): Full Bandwidth  
Open: Reduced Bandwidth
5. LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10K $\Omega$  resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
6. VeeR and VeeT may be internally connected within the SFP module.
7. RD-/+ : These are the differential receiver outputs. They are AC coupled 100 $\Omega$  differential lines which should be terminated with 100 $\Omega$  (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 370 and 2000 mV differential (185 – 1000 mV single ended) when properly terminated.
8. VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V  $\pm$ 5% at the SFP connector pin. Maximum supply current is 300 mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 $\Omega$  should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.
9. TD-/+ : These are the differential transmitter inputs. They are AC-coupled, differential lines with 100 $\Omega$  differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 500 mV – 2400 mV (250 mV – 1200 mV single-ended), though it is recommended that values between 500 and 1200 mV differential (250 mV– 600 mV single-ended) be used for best EMI performance.

DIMENSIONS



ORDERING INFORMATION

SFP Models	
Part Number	Description
SFP-M-TAA	1 Gigabit SFP Transceiver, Multi Mode 550M / LC / 850nm, 0~70°C, with TAA Compliant
SFP-M-T-TAA	1 Gigabit SFP Transceiver, Multi Mode 550M / LC / 850nm, -40~85°C, with TAA Compliant

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