

SFP-S40(-T)-TAA





FEATURES

- Hot Pluggable
- SFP Multi-Source Agreement Compliant
- LC Duplex Receptacle
- Single +3.3V Power Supply
- 1.25Gbps Gigabit Ethernet Compliant
- Class 1 Laser Safety Standard IEC 825 Compliant

GIGABIT FIBER SFP MODULE WITH TAA COMPLIANT

- Serial ID Functionality Support
- 1310nm DFB Laser
- 40km Reach
- Low Power Dissipation
- TAA Compliant

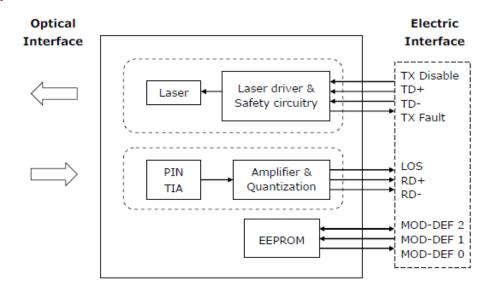


INTRODUCTION

The SFP-S40(-T)-TAA is specifically designed for the high performance integrated duplex data link over single-mode optical fiber. This transceiver module is compliant with the Small Form-factor Pluggable (SFP) Multisource Agreement (MSA). With the hot pluggability, these modules offer an easy way to be installed into SFP MSA compliant ports at any time without the interruption of the host equipment operating online.

The SFP-S40(-T)-TAA transceivers using a long wavelength (1310nm) DFP laser diode enable data transmission up to 40 km on a single-mode (9/125μm) optical fiber.

BLOCK DIAGRAM



The transceiver fundamentally consisted of two parts: transmitter and receiver. The transmitter features a TTL logic level Disable signal and a Fault indicator. The receiver features a TTL logic Loss of Signal (LOS) detection. For the access of serial identification information, an EEPORM is used to store the required data via the 2-wire serial CMOS EEPROM protocol. The detailed signal descriptions are listed in the following sections.

SPECIFICATIONS

Absolute Maximum Ratings							
Parameter	Symbol	Minimum	Maximum	Unit	Note		
Storage Temperature	Ts	-40	85	°C			
Supply Voltage	VccT VccR	-0.5	4.0	V			
Storage Relative Humidity	RH	5	95	%			

Recommended Operating Conditions							
Parameter	Symbol	Minimum	Туре	Maximum	Unit	Note	
Case Operating	Tc	0		70	°C	Refer to Ordering Information	
Temperature	TC .	-40		85	C		
Supply Voltage	Vcc	3.1	3.3	3.5	V		
Supply Current	$I_{TX} + I_{RX}$		180	300	mA		

Transmitter Electro-Optical Interface						
Parameter	Symbol	Minimum	Туре	Maximum	Unit	Note
Transmitter Differential Input Voltage	TD+/-	400		2400	mV	
TX_Disable - High	VDISABLE_H	2		Vcc	V	
TX_Disable - Low	VDISABLE_L	Vee		Vee+0.8	V	
TX_Fault - High	VFAULT_H	2		Vcc	V	

TX_Fault - Low	VFAULT_L	Vee		Vee+0.8	V	
Optical Output Power	Po	-2	1	3	dBm	1
Optical Extinction Ratio	Er	9			dB	
Center Wavelength	λc	1270	1310	1355	nm	
Spectral Width (RMS)	Δλ			1	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Optical Rise / Fall Time	t _r / t _f			260	ps	2
Relative Intensity Noise	RIN			-120	dB/Hz	
Total Contributed Jitter	TJ			227	ps	

Notes:

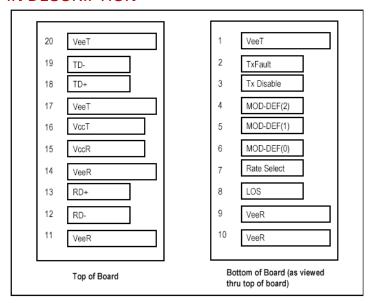
- Coupling into a 9/125 μm single-mode fiber. 20% to 80% value

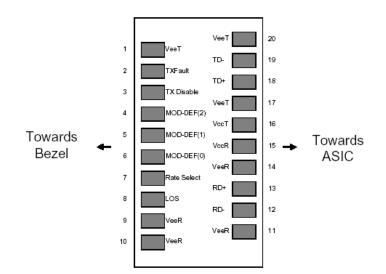
Receiver Electro-Optical Interface						
Parameter	Symbol	Minimum	Туре	Maximum	Unit	Note
Receiver Differential Output Voltage	RD +/-	400		2000	mV	
Receiver Overload	$P_{IN}MAX$	-3			dBm	1
Receiver Sensitivity	P _{IN} MIN			-23	dBm	1
Operating Center Wavelength	λc	1270		1580	nm	
Return Loss	RL	12			dB	
Receiver Loss of Signal - TTL Low	P_{RX_LOSD}			-23	dBm	
Receiver Loss of Signal - TTL High	P_{RX_LOSA}	-35			dBm	
Receiver Loss of Signal - Hysteresis	P _{RX_LOSH}	0.5			dB	

Notes:

With BER better than or equal to 1×10^{-12} , measured in the center of the eye opening with PRBS 2^7 –1

PIN DESCRIPTION





SFP Transceiver Electric Pad Layout

Diagram of Host Board Connector Block Pin Numbers and Names

Pin No.	Pin Name	Function	Plug Sequence	Notes
1	VeeT	Transmitter Ground	1	
2	TX Fault	Transmitter Fault Indication	3	1
3	TX Disable	Transmitter Disable	3	2
4	MOD-DEF 2	Module Definition 2	3	3
5	MOD-DEF 1	Module Definition 1	3	3
6	MOD-DEF 0	Module Definition 0	3	3
7	Rate Select	Select Between Full or Reduced Receiver Bandwidth	3	4
8	LOS	Loss of Signal	3	5
9	VeeR	Receiver Ground	1	6
10	VeeR	Receiver Ground	1	6
11	VeeR	Receiver Ground	1	6
12	RD -	Inv. Receiver Data Out	3	7
13	RD +	Receiver Data Out	3	7
14	VeeR	Receiver Ground	1	6
15	VccR	Receiver Power	2	8
16	VccT	Transmitter Power	2	8
17	VeeT	Transmitter Ground	1	6
18	TD+	Transmitter Data In	3	9
19	TD -	Inv. Transmitter Data In	3	9
20	VeeT	Transmitter Ground	1	6

Notes:

Plug Sequence: Pin engagement sequence during hot plugging.

- TX Fault is an open collector/drain output, which should be pulled up with a 4.7K 10KΩ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
- TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a $4.7-10~\mathrm{K}\Omega$ resistor. Its states are: Low (0 - 0.8V): Transmitter on

(>0.8, < 2.0V): Undefined

High (2.0 - 3.465V): Transmitter Disabled

Open: Transmitter Disabled

- Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K 10KΩ resistor on the host board. The pull-up voltage shall be VccT or VccR
 - Mod-Def 0 is grounded by the module to indicate that the module is present

Mod-Def 1 is the clock line of two wire serial interface for serial ID

Mod-Def 2 is the data line of two wire serial interface for serial ID

This is an optional input used to control the receiver bandwidth for compatibility with multiple data rates (most likely Fiber Channel 1x and 2x Rates). If implemented, the input will be internally pulled down with > $30k\Omega$ resistor. The input states are:

Low (0 - 0.8V): Reduced Bandwidth

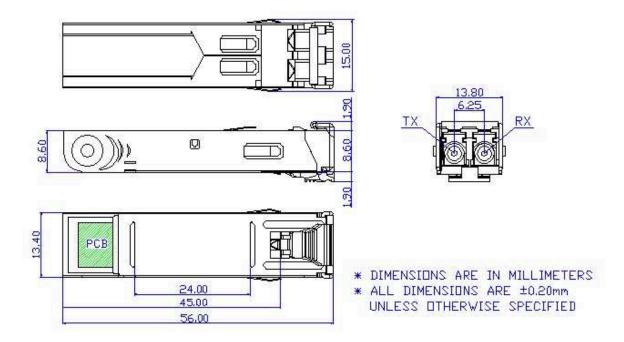
(>0.8 , < 2.0V): Undefined

High (2.0 V -3.465V): Full Bandwidth

Open: Reduced Bandwidth

- LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K -10K\Omega resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
- VeeR and VeeT may be internally connected within the SFP module.
- RD-/+: These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 370 and 2000 mV differential (185 - 1000 mV single ended) when properly terminated.
- VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V ±5% at the SFP connector pin. Maximum supply current is 300 mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1Ω should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA greater than the steady state value. VccR and VccT may be internally connected within the SEP transceiver module.
- TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 500 mV - 2400 mV (250 mV -1200 mV single-ended), though it is recommended that values between 500 and 1200 mV differential (250 mV- 600 mV single-ended) be used for best EMI performance.

DIMENSIONS



ORDERING INFORMATION

SFP Models				
Part Number	Description			
SFP-S40-TAA	1 Gigabit SFP Transceiver, Single-Mode 40km / LC / 1310nm, 0~70°C, with TAA Compliant			
SFP-S40-T-TAA	1 Gigabit SFP Transceiver, Single-Mode 40km / LC / 1310nm, -40~85°C, with TAA Compliant			

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